	Application No.	Applicant(s)
Notice of Allowability	Application No.	Application
	10/040,460	KITAHARA ET AL.
	Examin r	Art Unit
	Thinh T Nguyen	2818
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT IS of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in i) or other appropriate comministing the comministing of the	n this application. If not included unication will be mailed in due course. THIS
1. This communication is responsive to 7/21/2003.		
2. The allowed claim(s) is/are 1-19.		
3. The drawings filed on <u>09 January 2003</u> are accepted by t	he Examiner.	
 4.		
 Certified copies of the priority documents have been received. 		
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
5. Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).		
(a) The translation of the foreign language provisional application has been received.		
6. Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
7. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
 8. CORRECTED DRAWINGS must be submitted. (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No (b) including changes required by the proposed drawing correction filed, which has been approved by the Examiner. 		
(c) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No.		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet.		
9. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT FOR		
Attachm nt(s)		
 Notice of References Cited (PTO-892) Notice of Draftperson's Patent Drawing Review (PTO-948) Information Disclosure Statements (PTO-1449), Paper No. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	4☐ Interview 6☐ Examine	f Informal Patent Application (PTO-152) v Summary (PTO-413), Paper No er's Amendment/Comment er's Statement of Reasons for Allowance
	<u>.,</u>	

Application/Control Number: 10/040,460 Page 2

Art Unit: 2818

DETAILED ACTION

Reason for allowance

1. Claims 1-19 are allowed. The following is an examiner's statement of reason for allowance: None of the references of record teaches or suggests the claimed "SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH ENHANCED LAYOUT "having:

arranged in one direction, wherein a first region between the first power wiring and the second power wiring; fundamental circuit units constituted by combining one or more PMOS transistors and one or more NMOS transistors, the fundamental circuit units being arranged along with the first power wiring and the second power wiring; logic circuit units constituted by a plurality of the fundamental circuit units; functional circuit units constituted by a plurality of the logic circuit units, the functional circuit units being connected each other; wherein at least one part of the PMOS transistors and that of the NMOS transistors are arranged below the first power wiring and the second power wiring; and unit connection wirings to connect between the fundamental circuit units or to connect between the logic circuit units, of which terminals terminate at the functional circuit units are arranged on second regions that are other than the first region, in case the unit connection wirings are constituted by a layer that is same as a wiring layer that

Application/Control Number: 10/040,460

Art Unit: 2818

constitutes the first power wiring and the second power wiring or by wiring layers that are under the wiring layer of the first and second power wirings. " --

In claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

2. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Thinh T Nguyen whose telephone number is 703-305-0421. The examiner can normally be reached on Monday-Friday 8:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 703-308-4910. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9319 for After Final communications.

Application/Control Number: 10/040,460 Page 4

Art Unit: 2818

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Thinh T Nguyen TTN

Art unit 2818

Supervisory Patent Examiner Technology Center 2800